

## EAST Search History

| Ref # | Hits  | Search Query   | DBs                           | Default Operator | Plurals | Time Stamp          |
|-------|-------|--|-------------------------------|------------------|---------|---------------------|
| L1    | 246   | (soc system adj2 chip).ti.   | USPAT;<br>EPO                 | OR               | OFF     | 2006/09/05<br>13:00 |
| L2    | 102   | (soc system adj2 chip).ti. and<br>g06f\$.ipc.  | USPAT;<br>EPO                 | OR               | OFF     | 2006/09/05<br>13:00 |
| L3    | 3     | (soc system adj2 chip).ti. and<br>g06f\$.ipc. and 713/3\$.ccls.  | USPAT;<br>EPO                 | OR               | OFF     | 2006/09/05<br>13:02 |
| L4    | 9     | ("20030196127"  <br>"20040268278"  <br>"20050094036"   "5167024"  <br>"5768602"   "5978923"  <br>"6446213"   "6535982"  <br>"6665802").PN. | US-PGPUB<br>; USPAT;<br>USOCR | OR               | OFF     | 2006/09/05<br>13:02 |
| L5    | 118   | (soc system adj chip).ti.  | USPAT;<br>EPO                 | OR               | OFF     | 2006/09/05<br>13:03 |
| L6    | 94    | (system adj chip).ti.  | USPAT;<br>EPO                 | OR               | OFF     | 2006/09/05<br>13:06 |
| L7    | 32    | (soc).ti.  | USPAT;<br>EPO                 | OR               | OFF     | 2006/09/05<br>13:03 |
| L8    | 0     | ("system on a chip").ti.   | USPAT;<br>EPO                 | OR               | OFF     | 2006/09/05<br>13:05 |
| L9    | 47    | (system adj chip).ti. and<br>power   | USPAT;<br>EPO                 | OR               | OFF     | 2006/09/05<br>13:06 |
| L10   | 3     | (system adj chip).ti. and<br>power same size same cost   | USPAT;<br>EPO                 | OR               | OFF     | 2006/09/05<br>13:07 |
| L11   | 7     | (system adj chip).ti. and size<br>same cost  | USPAT;<br>EPO                 | OR               | OFF     | 2006/09/05<br>13:07 |
| S1    | 1     | "6157979".pn.  | USPAT;<br>EPO                 | OR               | OFF     | 2006/09/05<br>09:20 |
| S2    | 0     | preserv\$6 near2 context with<br>SOC   | USPAT;<br>EPO                 | OR               | OFF     | 2006/09/05<br>08:44 |
| S3    | 0     | preserv\$6 near2 context with<br>system adj2 chip  | USPAT;<br>EPO                 | OR               | OFF     | 2006/09/05<br>08:44 |
| S4    | 369   | preserv\$6 near2 context   | USPAT;<br>EPO                 | OR               | OFF     | 2006/09/05<br>08:44 |
| S5    | 20321 | (soc system adj2 chip) and<br>context  | USPAT;<br>EPO                 | OR               | OFF     | 2006/09/05<br>13:00 |

## EAST Search History

|     |        |  |                                  |    |     |                     |
|-----|--------|--|----------------------------------|----|-----|---------------------|
| S6  | 44     | (soc system adj2 chip) and context near3 preserv\$5                        | US-PGPUB<br>; USPAT;<br>EPO; JPO | OR | OFF | 2006/09/05<br>09:21 |
| S7  | 948675 | (soc system adj2 chip) and context near3 preserv\$5 amd g06f\$.ipc.        | US-PGPUB<br>; USPAT;<br>EPO; JPO | OR | OFF | 2006/09/05<br>09:21 |
| S8  | 5      | (soc system adj2 chip) and context near3 preserv\$5 and g06f\$.ipc.        | US-PGPUB<br>; USPAT;<br>EPO; JPO | OR | OFF | 2006/09/05<br>10:10 |
| S9  | 4518   | (soc system adj2 chip) with (power size cost\$1)                           | US-PGPUB<br>; USPAT;<br>EPO; JPO | OR | OFF | 2006/09/05<br>10:11 |
| S10 | 42     | (soc system adj2 chip) with (power with size with cost\$1)                 | US-PGPUB<br>; USPAT;<br>EPO; JPO | OR | OFF | 2006/09/05<br>10:11 |
| S11 | 23     | (soc system adj2 chip) with (power with size with cost\$1) and g06f\$.ipc. | US-PGPUB<br>; USPAT;<br>EPO; JPO | OR | OFF | 2006/09/05<br>10:11 |